

PATENT APPLICATION Attorney's Do. No. 9898-277 CLIENT No. SS-17652-US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of: Jae-Kyu Lee

Serial No. 10/626,248

Examiner:

Christian D. Wilson

Filed:

July 23, 2003

Group Art Unit:

2824

For:

METHOD FOR FORMING SHALLOW WELL OF

SEMICONDUCTOR DEVICE USING LOW-ENERGY ION

IMPLANTATION

Confirmation No.

1012

TRANSMITTAL LETTER

Mail Stop ISSUE FEE Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Enclosed for filing in the above-referenced application are the following:

Publication and Issue Fee

Limited Recognition Under 37 CFR §10.9(b)

Applicant's Comments on Examiner's Statement of Reasons for Allowance

PTO Form 2038 authorizing credit card payment for the above-listed fees

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Customer No. 20575

Respectfully submitted,

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Limited Recognition Under 37 CFR §10.9(b)

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I hereby certify that this correspondence is being transmitted to the U.S. Patent and Trademark Office via facsimile number (703) 746-4000, on November 23, 2004.

Name: Jennifer Abernathy



PATENT APPLICATION ATTORNEY DOCKET No. 9898-277 CLIENT NO. SS-17652-US

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Inventor(s):

Jae-Kyu LEE

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Title:

METHOD FOR FORMING SHALLOW WELL OF SEMICONDUCTOR

DEVICE USING LOW-ENERGY ION IMPLANTATION

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APPLICANT'S COMMENTS ON EXAMINER'S STATEMENT OF REASONS FOR ALLOWANCE

The Examiner's statement of reasons for Allowance lists some but not all, of the reasons for allowance. For example, each dependent claim contains additional limitations that may define over the prior art. The applications note that it is a well known tenet of patent law that each allowed patent claim stands alone. Further, although the Examiner has indicated at least one reason for allowance, there are other reasons that claims are allowable.

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Respectfully submitted,

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